

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Gopakumar Parameswaran, Cuong C. Ly, Douglas L. Yanagawa,
Mark N. Yamashita and Yuval Bachar
Assignee: Cisco Technology, Inc.
Title: SYSTEM AND METHOD FOR REDUCING CROSSTALK
BETWEEN VIAS IN A PRINTED CIRCUIT BOARD
Application No.: Unassigned Filing Date: Herewith
Examiner: Unassigned Group Art Unit: Unassigned
Docket No.: CIS0203US

Austin, Texas
December 4, 2003

MAIL STOP PATENT APPLICATION
COMMISSIONER FOR PATENTS
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ALEXANDRIA, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT
37 CFR § 1.97(b)

Dear Sir:

Pursuant to 37 CFR § 1.56, § 1.97 and § 1.98, the documents listed on the accompanying PTO Form-1449 are called to the attention of the Examiner for the above patent application. Pursuant to 1276 OG 55, wherein the USPTO waived the requirement under 37 CFR 1.98 (a)(2)(i) for submitting a copy of each cited U.S. patent and each U.S. patent application publication, no copies of U.S. patents or U.S. patent applications are enclosed. Copies of any listed foreign patent documents, pending non-published U.S. patent applications, and non-patent literature are enclosed.

Citation of these documents shall not be construed as:

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2. a representation that a search has been made; or
3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

EXPRESS MAIL NUMBER:

EV 304738421 US

Respectfully submitted,

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U.S. Department of Commerce, Patent and Trademark Office					Atty Docket No.		Serial No.	
					CIS0203US		Unassigned	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT					Applicants			
(Use several sheets if necessary)					Gopakumar Parameswaran, et al.			
					Filing Date		Group	
					Herewith		Unassigned	
U.S. Patent Documents								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
	AA							
	AB							
	AC							
	AD							
	AE							
	AF							
Patent Application Publication								
		Publication Number	Pub. Date	Name	Class	Subclass	Filing Date If Appropriate	
	AG	2003/0147375 A1	Aug. 7, 2003	Goergen, et al.	370	351		
Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	AH							
	AI							
	AJ							
	AK							
	AL							
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
	AM	GREEN, Lynne, pcb-update, "Crosstalk for Printed Circuit Board Designers," http://asp.pcb-update.com/data/newsletter/pcb-update-news-vol-2.pdf , January 2001, pp. 1-12.						
	AN	Cadence Design Systems, Inc., "Inside Differential Signals," Cadence Community Education Series – December 2001, http://www.specctraquest.com/Downloads/InsideDiffSigals_Green.qxd.pdf , pp. 1-4.						
	AO	BELOPOLSKY, Yakov and ELLIS, John, FCI Electronics, Inc., "Modular Connectors For High Speed Differential Signal Transmission," http://www.fciconnect.com/products/technical_01_06.asp , pp. 1-12 (printed on November 19, 2003).						
	AP							
Examiner			Date Considered					
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.								